

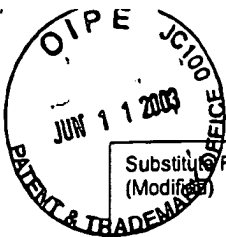
Substitute Form PTO-1449 (Modified) Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-601001 ✓	Application No. 09/942,116 ✓
	Applicant William R. Wheeler et al. ✓		
	Filing Date August 29, 2001 ✓	Group Art Unit 2185 ✓	

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
2	AA	2002/0049957	04/2002	Hosono et al.			
2	AB	6,233,723	05/2001	Pribetich			
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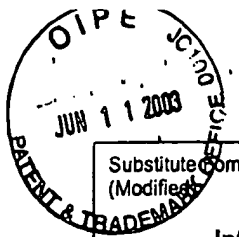
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2	ABBB						

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Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	ADDD							
	AEEE							
	AFFF							

Other Documents (include Author, Title, Date, and Place of Publication)		
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<i>z</i>	AGGG	Foley et al., "An Object Based Graphical User Interface for Power Systems", IEEE Transactions on Power Systems, Vol. 8, No. 1, February 1993, pp. 97-104.
<i>z</i>	AHHH	Pedram et al., "Floorplanning with Pin assignment", 1990 IEEE International Conference on Computer-Aided Design, 11 November 1990, pp. 98-101.
<i>z</i>	AIHH	Renoir, HDL Design Datasheet, Mentor Graphics, 1-8, 1999.
<i>z</i>	AJJJ	Mentor Graphics Corporation, Renoir™ With HDL2Graphics™, pp. 1-6, 1998, Oregon.
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(37 CFR §1.98(b))

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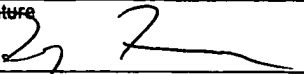
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	AQ	Parlakhbalek, et al., "A Multiple-Strength Multiple-Delay Compiled-Code Logic Simulator", <i>IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems</i> , 12(12):1937-1946 (1993).
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	AS	
	AT	

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	Applicant William R. Wheeler et al.		
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	AG						
	AH						
	AI						
	AJ						
	AK						

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	AN							
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	AP							

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<i>JS</i>	AQ	Computer Design, "AFTER HARD KNOCKS, CYCLE-BASED SIMULATORS STAND THEIR GROUND". http://www.computer-design.com/Editorial/1996/10/ASIC/after.html , accessed on August 23, 2001, pages 1-5.
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